



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Akira Matsuda et al.

Group Art Unit: 1794

Serial Number: 10/719,020

Examiner: Michael E. Lavilla

Filed: November 24, 2003

P.T.O. Confirmation No.: 9168

For: **METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER**

Attorney Docket No.: 032130

Customer Number: 38834

DECLARATION UNDER 37 CFR §1.132

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, Akira Matsuda, hereby declare and state that:

(1) I am familiar with the contents of the United States Patent Application Serial No. 10/719,020, filed on November 24, 2003, claiming the priority of Japanese Patent Application No. 2002-341813 filed on November 26, 2002.

(2) I am one of the inventors of the invention of United States Patent Application Serial No. 10/719,020.

(3) I prepared a declaration under 37CFR§ 1.132 ("1st Declaration") on March 5, 2006, which

*considered
me
8/31/08*